09/980891

AC4 Rec'd PCT/PTO 17 DEC 2001

ON DISCLOSÙRE STATEMENT TRANSMITTAL OF INFORMA Docket No. (Under 37 CFR 1.97(b) or 1.97(c)) 02008.071001; AD-0243PCTUS In Re Application Of: Masahiro ISHIDA et al. Serial No. Examiner Group Art Unit Filing Date METHOD AND APPARATUS FOR DEFECT ANALYSIS OF SEMICONDUCTOR INTEGRATED CIRCUIT Address to: **Assistant Commissioner for Patents** Washington, D.C. 20231 37 CFR 1.97(b) The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application; within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or before the mailing date of a first Office Action on the merits, whichever event occurs last. 37 CFR 1.97(c) The Information Disclosure Statement submitted herewith is being filed after three months of the filing 2. of a national application, or the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; or after the mailing date of a first Office Action on the merits, whichever occurred last but before the mailing date of either: 1. a Final Action under 37 CFR 1.113, or 2. a Notice of Allowance under 37 CFR 1.311, whichever occurs first. Also submitted herewith is: a certification as specified in 37 CFR 1.97(e); OR the fee set forth in 37 CFR 1.17(p) for submission of an Information Disclosure Statement under 37 CFR 1.97(c).

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TRANSMITTAL OF INFORMA. ON DISCLOSURE STATEMENT			Docket No.
(Under 37 CFR 1.97(b) or 1.97(c))			02008.071001; AD-0243PCTUS
In Re Application Of: Masahiro ISHIDA et al.			
Serial No.	Filing Date	Examiner	Group Art Unit
Title: METHOD AND APPARATUS FOR DEFECT ANALYSIS OF SEMICONDUCTOR INTEGRATED CIRCUIT			
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